



Sept Generations DExecuteurs 1688-1847 Memoires Des Sanson MIS En Ordre (2)

By -

RareBooksClub. Paperback. Book Condition: New. This item is printed on demand. Paperback. 66 pages. Original publisher: Aberdeen Proving Ground, MD : Army Research Laboratory, 1999 OCLC Number: (OCoLC)311097378 Subject: Computational fluid dynamics. Excerpt: . . . This report will discuss how this effect was SGI Power Challenge and on the Convex Exemplar approached, what the results were, and the reasons for those results. 2. The Architecture of the SGI Power Challenge Before one can discuss the results of this effort, it is helpful to have a short introduction to the architecture involved. The SGI Power Challenge can be thought of as the prototypical RISC-based cache-coherent shared memory SMP. As such, it will be discussed first. From the hardware perspective, it has the following properties: (1) Powerful RISC-based processors. In this case, R5000 processors rated at 300 MFLOPS each. (2) This version of the Power Challenge had up to 18 processors and up to 16 GB of memory, although one could not configure a system with both 18 processors and 16 GB of memory. (3) Each processor had a large off-chip cache (4 MB). (4) Hardware protocols maintain the coherency between...



READ ONLINE
[3.02 MB]

Reviews

The most effective pdf i possibly read. It is amongst the most amazing publication i actually have go through. You are going to like the way the author publish this pdf.

-- **Chelsea Durgan PhD**

I actually started off looking over this pdf. I am quite late in start reading this one, but better then never. Once you begin to read the book, it is extremely difficult to leave it before concluding.

-- **Mr. Bertrand Anderson DDS**